EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with James Long on October 23, 2009.

The application has been amended as follows:

Regarding claim 8,

1) Please amend the claim limitation disclosed in line 7 of the claim, "*... have the same driving polarity, ...*" as follows: "*... have same driving polarity, ...*".

2) Please amend the claim limitation disclosed in line 10 of the claim, "*wherein all the of pixels ...*" as follows: "*wherein all of the pixels ...*".

3) Please amend the claim limitation disclosed in line 12 of the claim, "*the pixels substantially have the same driving polarity;*" as follows: "*the pixels substantially have same driving polarity,*".

4) Please amend the claim limitation disclosed in lines 20-21 of the claim, "*all of the alternating data lines respectively provide the pixel voltages having opposite polarity; and*" as follows: "*all of the adjacent data lines provide pixel voltages having opposite polarities to each other, respectively; and*"

5) Please amend the claim limitation disclosed in lines 22-23 of the claim, "*a plurality of gate lines, wherein the r^{th} gate line is used for turning on all only odd pixels in*

the j^{th} and the $(j+1)^{th}$ pixel sets …" as follows: "*a plurality of gate lines, wherein r^{th} gate line is used for turning on only all odd pixels in the j^{th} and the $(j+1)^{th}$ pixel sets …*"

2. The following is an examiner's statement of reasons for allowance:

None of the cited prior arts teaches a pixel array comprising a plurality of gate lines, wherein r^{th} gate line is used for turning on only all odd pixels in j^{th} and $(j+1)^{th}$ pixel sets of the i^{th} row of the pixels and only all even pixels in the j^{th} and the $(j+1)^{th}$ pixel sets of the $(i+1)^{th}$ row of the pixels, where i, j, and r are positive integers and wherein all of the adjacent data lines provide pixel voltages having opposite polarities to each other, respectively.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Seokyun Moon whose telephone number is 571-272-5552. The examiner can normally be reached on 8:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on 572-272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

October 23, 2009
/S. M./
Examiner, Art Unit 2629

/Sumati Lefkowitz/
Supervisory Patent Examiner, Art Unit 2629